

**IN THE UNITED STATES  
PATENT AND TRADEMARK OFFICE**

APPLICANTS: Donald E. Steiss et al.  
APPLICATION NO.: 10/733,153  
FILING DATE: December 10, 2003  
TITLE: Thread Interleaving in a Multithreaded Embedded Processor  
EXAMINER: William M. Treat  
GROUP ART UNIT: 2181  
ATTY. DKT. NO.: 22347-08260 (8116)

BOX AMENDMENT  
COMMISSIONER OF PATENTS  
P.O. BOX 1450  
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**RESPONSE TO RESTRICTION REQUIREMENT**

SIR:

The following remarks are responsive to the office action dated August 9, 2006,  
received in the patent application identified above.